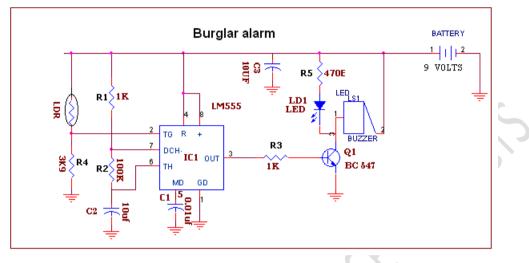
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## **BURGLAR ALARM:**



This is a LDR based monostable trigger circuit getting activated in the dark. The LDR resistance goes high in the dark and it is triggered by a negative going voltage decided by R4 connected at Pin no.2 of IC 1. The input when triggered, switches ON the output for atleast 2 seconds which is decided by R1, R2 & C2. A Buzzer is connected at its output, using a NPN transistor (BC547). The alarming time can be increased by increasing the value of R2 or the capacitor C2. The Circuit is operated with a 9V battery.

Components used: R1 = 1k resistor, R2 = 100k resistor. R3 = 1k Resistor.R4 = 3k9 resistor,R5 = 470E resistor C1 = 0.01uf capacitor,C2 = 10uf capacitor,C3 = 10uf capacitor,Q1 = BC 547 NPN Transistor,LD1 = L E D IC 1 = LM 555 (with base), LDR, Battery = 9volt, Battery snapper, Buzzer

